

Abstract of the Disclosure

A signal amplifier and an integrated circuit capable of obtaining a high gain and high output in a wide range. The signal amplifier includes a pre-stage circuit for inputting a signal from outside and a post-stage circuit for amplifying the signal output from the pre-stage circuit and outputting it. The input impedance of the pre-stage circuit and the output impedance of the post-stage circuit are set so as to match with an external impedance within a frequency range of one digit or above. The output impedance of the pre-stage circuit and the input impedance of the post-stage circuit are set so as to match with an impedance lower than the output impedance of the post-stage circuit.